

# Process: SG-6 with HV 20V/30V/36V

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## 0.7um LV CMOS With HV CMOS for Analog IC

### Key Features

LV CMOS for Digital and Analog circuit

Operating Voltage 5.0V (5.5V Max.)

Min. Gate Length 0.8um

Vtp / Vtn [V] 0.65 / 0.65 (Typ.)

Low Vtp / Vtn [V] 0.50 / 0.50 (Typ.)

Capacitor

MOS · PIP

Resistor

Poly · HR Poly · N- · P- · etc.

Embedded Devices

HV MOS ( HV20/ HV30/ HV36)

Max. Drain Voltage HV20/ HV30/ HV36 20V/ 30V/ 36V

Max. Gate Voltage same as Drain

Substrate PNP for Voltage reference

8.0

Vbe 0.65V @ Ic=1uA

Photo Diode

感度ピーク 780nm